

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

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|------------------------|-------------------|
| Application Number | 10805596 |
| Filing Date | 2004-03-19 |
| First Named Inventor | Chieng et al. |
| Art Unit | 2193 |
| Examiner Name | Yaary, Michael D. |
| Attorney Docket Number | ELAN-01167US1 |

U.S.PATENTS

| Examiner Initial* | Cite No | Patent Number | Kind Code† | Issue Date | Name of Patentee or Applicant of cited Document | Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear |
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|--------|---|----------|--|------------|---------------|--|
| /M.Y./ | 9 | 74774722 | | 2009-01-06 | Chieng et al. | |
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| /M.Y./ | 1 | 20020105448 | | 2002-08-08 | Friedhof | |
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| /M.Y./ | 2 | JP 2002-314429 | JP | | 2002-10-25 | Sony Corp. | | <input checked="" type="checkbox"/> |
| /M.Y./ | 3 | JP 2008-018518 | JP | | 2008-01-31 | Fujitsu Hitachi Plasma Display | | <input checked="" type="checkbox"/> |

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| /M.Y./ | 1 | KETOLA et al., "Synchronization of Fractional Interval Counter in Non-Integer Ratio Sample Rate Converters," Proceedings of the 2003 International Symposium on Circuits and Systems, 2003 ISCAS '03, vol. 2, May 25-28, 2003, pp. 89-92. | <input type="checkbox"/> |
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| Examiner Signature | /Michael Yaary/ | Date Considered | 07/14/2010 |
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